



2823
Attorney Docket No. 0756-2424

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Osamu NAKAMURA et al.

Serial No. 10/046,893

Filed: January 17, 2002

For: METHOD OF MANUFACTURING
SEMICONDUCTOR DEVICE

) Group Art Unit: 2823

) Examiner: H. Lee

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The references cited on PTO 1449 Form, Page 1 (List 1), are all patents of the Assignee (Semiconductor Energy Laboratory Co., Ltd.), which may relate to the subject application.

The references cited on PTO 1449 Form, Pages 1-4 (List 2), are references which were cited in U.S. Serial No. 10/056,055. U.S. Serial No. 10/056,055 corresponds to U.S. Patent Publication No. 2002/0134981 (List 1)

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

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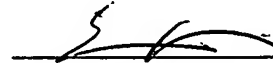
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/046,893
				Filing Date	January 17, 2002
				First Named Inventor	Osamu Nakamura et al.
				Group Art Unit	2823
				Examiner Name	H. Lee
Sheet	1 (List 1)	of	1	Attorney Docket Number	0756-2424



U.S. PATENT DOCUMENTS						
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		Number	Kind Code ² (if known)			
		6,204,154		Zhang et al.	03/20/2001	
		6,355,509		Yamazaki	03/12/2002	
		6,383,852		Zhang et al.	05/07/2002	
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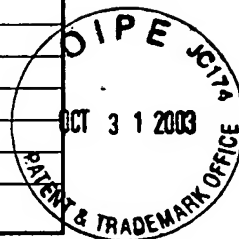
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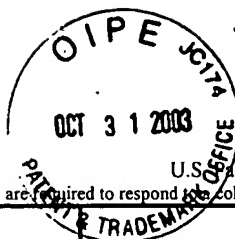
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				Filing Date	January 17, 2002
				First Named Inventor	Osamu Nakamura et al.
				Group Art Unit	2823
				Examiner Name	H. Lee
Sheet	1 (List 2)	of	4	Attorney Docket Number	0756-2424



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		Number	Kind Code ² (if known)			
		3,535,775		Garfinkel et al.	10/27/1970	
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				Filing Date	January 17, 2002
				First Named Inventor	Osamu NAKAMURA et al.
				Group Art Unit	2823
				Examiner Name	H. Lee
Sheet	2 (List 2)	of	4	Attorney Docket Number	0756-2424

U.S. PATENT DOCUMENTS						
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		Number	Kind Code ² (if known)			
		5,696,386		Yamazaki	12/09/1997	
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				First Named Inventor	Osamu NAKAMURA et al.
				Group Art Unit	2823
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Sheet	3 (List 2)	of	4	Attorney Docket Number	0756-2424

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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
		JP	07-130652			05/19/1995		AB
		JP	07-183540			07/21/1995		AB
		JP	08-078329			03/22/1996		AB
		JP	09-074207			03/18/1997		FULL
		JP	3032801			12/18/2000		AB
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		EP	0 651 431			05/03/1995		ENG

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		Masayasu Miyake et al., <i>Characteristics of Buried-Channel pMOS Devices with Shallow Counter-Doped Layers Fabricated Using Channel Preamorphization</i> , IEEE Transactions on Electron Devices, Vol. 43, No. 3, March 1996, pp. 444-449	
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